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Application Number

10/642,481

Applicant(s)

Enomoto, et al.

Filing Date

August 18, 2003

Group Art Unit

Not Yet Assigned

U.S. PATENT DOCUMENTS

[illegible]

~~FOREIGN PATENT DOCUMENTS~~

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Youichi HIDAKA, et al., Institute of Electronics, Information and Communication Engineers, society Meeting 2002, B-7-11 to B-7-13, "Proposal of Ethernet Architecture GOE (Global Optical Ethernet) for Future Generation", pp. 171-173.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.